## THE CLAIMS

## What is claimed is:

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- A system application comprising:

   a headset having a memory for storing a preference setting; and
   a host adapter coupled to the headset and having a performance
   parameter, wherein the host adapter is capable of accessing the memory in order to read the preference setting.
- 2. The system application of claim 1, wherein the host adapter accesses the memory through a serial port in order to read the preference setting from the memory.
- 3. The system application of claim 1, wherein the memory is implemented within a headphone of the headset.
- 4. The system application of claim 1, wherein the memory is implemented within a cable quick disconnect of the headset.
- 5. The system application as claimed in claim 1, wherein the host adapter adjusts its performance parameter in accordance with the preference setting read from the memory.
- 6. The system application of claim 3, wherein the preference setting is one of a preferred volume level, a preferred treble level, a preferred balance level.
- 7. The system application of claim 3, wherein the performance parameter of the host adapter may be further manually adjusted by a user to a

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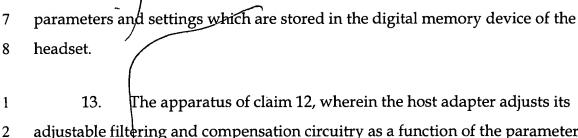
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- new preference level, which is then stored in the memory, thereby overwriting
   the previously stored preference setting.
- 8. An apparatus comprising a headset having at least one headphone for receiving audio signals, at least one microphone assembly for transmitting audio signals and a digital memory device for storing performance characteristics of the at least one headphone.
  - 9. The apparatus of claim 8 wherein the digital memory device stores filtering and compensation circuitry parameters and settings which are determined as a function of the performance characteristics of the at least one headphone.
  - 10. The apparatus of claim 8, further comprising a host adapter coupled to the headset having for transmitting audio signals to the at least one headphone of the headset and receiving audio signals from the at least one microphone assembly of the headset, said host adapter having a memory interface for accessing the digital memory device, in order to read the performance characteristics of the at least one headphone which are stored in the digital memory device of the headset.
  - 11. The apparatus of claim 10, wherein the host adapter adjusts the audio signals transmitted to the at least one headphone as a function of the performance characteristics read from the memory.
  - 12. The apparatus of claim 9, further comprising a host adapter coupled to the headset having for transmitting audio signals to the at least one headphone of the headset and receiving audio signals from the at least one microphone assembly of the headset, said host adapter having adjustable filtering and compensation circuitry and a memory interface for accessing the



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adjustable filtering and compensation circuitry as a function of the parameters and settings read from the memory.

digital memory device, in order to read the filtering and compensation circuitry

- 14. The apparatus as claimed in claim 8, wherein the performance characteristics of the at least one headphone include at least one of a receive signal frequency response, a receive signal sensitivity, a receive impedance characteristic, a receive signal gain, and a receive signal to noise ratio.
- 15. An apparatus comprising a headset having at least one headphone for receiving audio signals, at least one microphone assembly for transmitting audio signals and a digital memory device for storing performance characteristics of the at least one microphone assembly.
- 16. The apparatus of claim 15 wherein the digital memory device stores filtering and compensation circuitry parameters and settings which are determined as a function of the performance characteristics of the at least one microphone assembly.
- The apparatus of claim 15, further comprising a host adapter coupled to the headset for transmitting audio signals to the at least one headphone of the headset and receiving audio signals from the at least one microphone assembly of the headset; wherein the host adapter unit includes a memory interface for accessing the memory device in order to read the performance characteristics of the at least one microphone assembly stored in the digital memory device of the headset.

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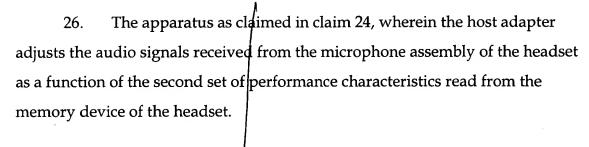
18.	The apparat	us of claim 17, wherein the host adapter adjusts the
audio signals	s received from	m the at least one microphone assembly of the headse
as a function	of the perføri	mance characteristics read from the memory.

- 19. The apparatus of claim 16, further comprising a host adapter coupled to the headset having for transmitting audio signals to the at least one headphone of the headset and receiving audio signals from the at least one microphone assembly of the headset, said host adapter having adjustable filtering and compensation circuitry and a memory interface for accessing the digital memory device, in order to read the filtering and compensation circuitry parameters and settings which are stored in the digital memory device of the headset.
- 20. The apparatus of claim 19, wherein the host adapter adjusts its adjustable filtering and compensation circuitry as a function of the parameters and settings read from the memory.
- 21. The apparatus as claimed in claim 15, wherein the performance characteristics of the at least one microphone include at least one of a transmit signal frequency response, a transmit signal sensitivity, a transmit impedance characteristic, a transmit signal gain, and a transmit signal to noise ratio.

22.	An apparatu	comprising:
	a headset having	at least one headphone for receiving audio signals
	and a mid	rophone assembly for transmitting audio signals,
	wherein	the headset further includes a memory device for
	storing a	first set of performance characteristics for the at least
	one head	phone and a second set of performance characteristics
	for the m	icrophone assembly.

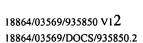
- 23. The apparatus as claimed in claim 22, further comprising:

  a host adapter coupled to the headset for transmitting audio signals to
  the at least one headphone and receiving audio signals from the
  microphone, wherein the host adapter includes a processor for
  accessing the memory device and reading either one or both of
  the first set of performance characteristics and the second set of
  performance characteristics.
- 24. The apparatus as claimed in claim 23, wherein the host adapter adjusts the audio signals transmitted to the at least one headphone as a function of the first set of performance characteristics read from the memory device of the headset.
- 25. The apparatus as claimed in claim 22, wherein the first set of performance characteristics include a receive frequency response of the at least one headphone, a receive audio level at the at least one headphone, a receive impedance characteristic of the at least one headphone, a receive signal gain of the at least one headphone, and a receive signal to noise ratio at the at least one headphone.



- 27. The apparatus as claimed in claim 22, wherein the second set of performance characteristics include a transmit frequency of the microphone assembly, a transmit audio level of the microphone assembly, an impedance characteristic of the microphone assembly, a transmit signal gain of the microphone assembly, and a signal to noise ratio of the microphone assembly.
- 28. The apparatus of claim 22 wherein the digital memory device stores a first set of filtering and compensation circuitry parameters and settings which are determined as a function of the first set of performance characteristics of the at least one headphone and a second set of filtering and compensation circuitry parameters and settings which are determined as a function of the second set of performance characteristics of the microphone assembly.
  - 29. The apparatus as claimed in claim 23, further comprising:

    a host adapter coupled to the headset for transmitting audio signals to
    the at least one headphone and receiving audio signals from the
    microphone assembly, wherein the host adapter includes:
    filtering and compensation circuitry, and
    a processor for accessing the memory device and reading
    either one or both of the first set of filtering and compensation
    circuitry parameters and the second set of filtering and
    compensation circuitry parameters.



1	30. The apparatus as claimed in claim 29, wherein the host adapter
2	adjusts its filtering and compensation circuitry in order to adjust the audio
3	signals transmitted to the at least one headphone as a function of the first set of
4	filter and compensation circuitry parameters read from the memory device of the
5	headset.
1	31. The apparatus as claimed in claim 29, wherein the host adapter
2	adjusts the its filtering and compensation circuitry in order to adjust the audio
3	signals received from the microphone assembly of the headset as a function of
4	the second set of filtering and compensation circuitry parameters read from the
5	memory device of the headset.
1	32. A process for manufacturing a headset having at least one
2	headphone and a microphone assembly, comprising the steps of:
3	enclosing a memory device within a headphone or cable quick
4	disconnect of the headset;
5	measuring performance characteristics of the headset; and
6	storing the performance characteristics in the memory device.
1	33. The process claimed in claim 32, wherein the step of measuring
2	performance characteristics of the headset includes:
3	coupling the headset to a test apparatus;
4	transmitting an audio test pattern from the test apparatus to the at
5	least one headphone of the headset; and
6	measuring performance characteristics of the at least one headphone.
1	34. The process claimed in claim 33, wherein performance
2	characteristics of the at least one headphone include a frequency response

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of the at least one headphone, a receive sensitivity of the at least one

4	headphone, a received signal audio level at the at least one headphone,	an
5	impedance characteristic of the at least one headphone, a received sign	al
6	gain at the at least one headphone, and a received signal-to-noise ratio	at
7	the at the at least one headphone.	
1	35. The process claimed in claimed 33, further comprising the steps	of:
2	determining a first set of filtering and compensation parameters as	a
3 ·	function of the performance characteristics of the at least one	•
4	headphone; and	
5	storing the first set of filtering and compensation parameters in the	
6	memory of the headset.	
1	36. The process claimed in claim 33, wherein the audio test pattern is	S
2	tored in the memory device for future reference.	
1	37. The process claimed in claim 32, wherein the step of measuring	
2	erformance characteristics of the headset includes:	
3	coupling the headset to a test apparatus;	
4	transmitting an audio test signal from the microphone assembly of	the
5	headset to the test apparatus; and	
6	measuring performance characteristics of the microphone assembly	•
1	38. The process claimed in claim 37, wherein performance	
2	haracteristics of the microphone assembly include a transmit signal audio lev	⁄el
3	om the microphone assembly, a transmit sensitivity of the microphone	
4	ssembly, an impedance characteristic of the microphone assembly, and a	
5	ransmit signal-to-noise ratio from the microphone assembly.	

	1	39.	The process claimed in claimed 37, further comprising the steps of:
	2	det	termining a second set of filtering and compensation parameters as
	3		a function of the performance characteristics of the microphone
	4		assembly; and
	5	sto	ring the first set of filtering and compensation parameters in the
	6		memory of the headset.
$\sim$	1	40.	The process of claim 37, wherein the audio test signal is stored in
0	2	the memory	device for future reference.
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ű	1	41.	A method for adjusting an audio signal provided to a headset
Į.	2	having at lea	st one headphone and a memory device, the method comprising:
Ü	3	sto	ring a performance characteristic of the at least one headphone in
	4		the memory device of the headset;
LJ a	5	re	ding the performance characteristic stored in the memory device;
H	6		and
	7	adj	justing the audio signal provided to the headset as a function of the
g g	8		performance characteristic read from the memory device.
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	1	42.	The method of claim 41 wherein the audio signal is provided to the
	2	headset from	a host adapter which automatically adjusts the audio signal using
	3	filtering and	compensation circuitry before it is provided to the headset as a
	4	function of the	ne performance characteristic read from the memory device.
	1	43.	The method of claim 42, wherein the memory device includes a
	2	serial port an	d further wherein the host adapter communicatively couples to the
	3	memory dev	ice through the serial port in order to read the performance
	4	characteristic	•
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44.	The method of claim	ed 41, wherein the performance characteristic
of the at least	one headphone is on	e of a frequency response of the at least one
headphone, a	receive sensitivity of	the at least one headphone, a received signal
audio level at	the at least one head	phone, an impedance characteristic of the at
least one head	phone, a received si	gnal gain at the at least one headphone, and a
received signa	ll-to-noise ratio at the	e at the at least one headphone.
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45. A method for adjusting an audio signal received from a headset having a microphone assembly and a memory device, the method comprising: storing a performance characteristic of the microphone assembly in the memory device of the headset; reading the performance characteristic stored in the memory device; and adjusting the audio signal received from the headset as a function of the performance characteristic read from the memory device.

- 46. The method of claim 45 wherein the audio signal is received from the headset by a host adapter and the host adapter automatically adjusts the audio signal received from the headset as a function of the performance characteristic read from the memory device.
- 47. The method of claim 46, wherein the memory device includes a serial port and further wherein the host adapter communicatively couples to the memory device through the serial port in order to read at least one of the plurality of performance characteristics.
- 48. The process claimed in claim 45, wherein performance characteristic of the microphone assembly is one of a frequency response of the microphone assembly, a transmit sensitivity of the microphone assembly, a



	4	transmit signal audio level of the microphone assembly, an impedance
	5	characteristic of the microphone assembly, a transmit signal gain from the
	6	microphone assembly, and a transmit signal-to-noise ratio of the microphone
	7	assembly.
	1	49. A method for adjusting an audio signal which is received from or
	2	transmitted to a headset having at least one headphone, a microphone, and a
<b>0</b> /	3	memory device, the method comprising:
Ø	4	storing a plurality of performance characteristics for the headset in the
•	5	memory device of the headset;
nem.	6	reading at least one of the plurality of performance characteristics from
	7	the memory device; and
Į.	8	adjusting the audio signal which is received from or transmitted to the
Ü	9	headset as a function of the at least one performance
64456353	10	characteristic read from the memory device.
	1	50. The method of claim 49 wherein a host adapter reads at least one of
	2	the plurality of performance characteristics from the memory device and either:
	3	receives the audio signal from the microphone of the headset and
J	4	adjusts the received audio signal in accordance with the
	5	performance characteristic read from the memory device of the
	6	headset; or
	7	receives the audio signal to be transmitted to the headset from an
	8	exterior system application, adjusts the audio signal as a
	9	function of the performance characteristic read from the
	10	memory device of the headset, and transmits the adjusted audio
	11	signal to the headset.
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	1	51. The method of claim 50, wherein the memory device includes a
	2	serial port to which the host adapter communicatively couples in order to read at
	3	least one of the plurality of performance characteristics from the memory device.
	1	52. An apparatus for storing and accessing information related to a
	2	headset comprising:
$\langle \rangle$	3	a memory device built into the headset.
()	1	53. The apparatus of claim 52, wherein the memory device stores a
	2	production date of the headset, a serial number of the headset, a service date and
	3	a type of service performed on the headset.
Ů M	1	54. The apparatus of claim 53, wherein the type of service performed
w Lu	2	may include a routine testing service, a routine maintenance service, a repair
	3	service or a replacement of parts service.
	1	55. A method for tracking a service history of a headset having a
o O	2	memory device, the method comprising:
ā	3	storing a service date and type of service performed on the headset in
	4	the memory device; and
	5	reading the service date and type of service performed on the headset
	6	from the memory.
	1	56. The method of claim 55, wherein the type of service performed
	2	may include a testing service, a maintenance service, a repair service or a
	3	replacement of parts service.
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1	57. A method for automatically setting performance parameters of a
2	host adapter to various user defined preferences for different users of the host
3	adapter, the method comprising:
4	storing a first set of user defined preferences for a first user in a
5	headset having a memory device;
6	storing a second set of user defined preferences for a second user in the
7	headset having the memory device;
8	retrieving the first set of user defined preferences from the memory
9	when the headset is coupled to the host adapter and used by the
10	first user and, thereafter setting each of the performance
<u>j</u> 11	parameters of the host adapter to the first set of user defined
<u> </u>	preferences retrieved from the memory; and
11 12 13 14 15	retrieving the second set of user defined preferences from the memory
Ī 14	when the headset is coupled to the host adapter and used by the
	second user, and thereafter setting each of the performance
16 17 17 17	parameters of the host adapter to the second set of user defined
] 17	preferences retrieved from the memory.
ā A	
1	58. The method of claim 57, wherein the first set of user defined
2	preferences include a volume level preferred by the first user, a bass level

- preferred by the first user, a treble level preferred by the first user, a balance 3 level preferred by the first user. 4
  - 59. The method of claim 57, wherein the second set of user defined preferences include a volume level preferred by the second user, a bass level preferred by the second user, a treble level preferred by the second user, a balance level preferred by the second user.

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1	60. A system for automatically setting performance parameters of a
2	host adapter to various user defined preferences for different users of the host
3	adapter, the system comprising:
4	the host adapter;
5	a headset with memory for storing a first set of user defined
6	preferences for a first user and storing a second set of user
7	defined preferences for a second user;
8	a memory interface within the host adapter for retrieving the first set
9	of user defined preferences when the headset with memory is
10	coupled to the host adapter and used by the first user, thereafter
11	setting a series of performance parameters of the host adapter to
12	the first set of user defined preferences; and retrieving the
13	second set of user defined preferences when the headset with
14	memory is coupled to the host adapter and used by the second
15	user, thereafter setting the performance parameters of the host
16	adapter to the second set of user defined preferences.
1	61. The system of claim 60, wherein the first set of user defined
2	preferences include a volume level preferred by the first user, a bass level
3	preferred by the first user, a treble level preferred by the first user, a balance
4	level preferred by the first user.
1	62. The system of claim 60, wherein the second set of user defined

balance level preferred by the second user.

preferences include a volume level preferred by the second user, a bass level

preferred by the second user, a treble level preferred by the second user, a

1	63. A host adapter for providing signals to and from a headset having
2	a memory device, the host adapter comprising:
3	an adjustable series of performance parameters for adjusting the
4	signals provided to and from the headset;
5	a memory interface for retrieving a first set of user defined preferences
6	when the headset with memory device is coupled to the host
7	adapter and used by a first user, the host adapter thereafter
8	setting the adjustable series of performance parameters of the
9	host adapter to the first set of user defined preferences in order
_ 10_0	to adjust the signals provided to and from the headset with
₫ 11 \X	memory in accordance with the first set of user defined
广 月12	preferences; and retrieving a second set of user defined
10 11 11 12 13 13 14	preferences when the headset with memory is coupled to the
月 14	host adapter and used by a second user, the host adapter
15	thereafter setting the adjustable series of performance
<u>u</u> 16	parameters of the host adapter to the second set of user defined
16 17 5 18	preferences in order to adjust the signals provided to and from
☐ 18	the headset in accordance with the second set of user defined
19	preferences.

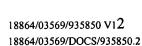
- 64. The system of claim 63, wherein the adjustable series of performance parameters include a volume level, a bass level, a treble level, and a balance level.
- 65. The system of claim 63, wherein the first set of user defined preferences include a volume level preferred by the first user, a bass level preferred by the first user, a treble level preferred by the first user, a balance level preferred by the first user.

66.	The system of claim 63, wherein the second set of user defined
preferences	include a volume level preferred by the second user, a bass level
preferred by	the second user, a treble level preferred by the second user, a
balance leve	l preferred by the second user.

67. A host adapter coupled to a headset having at least one headphone, a microphone assembly and a memory device, the host adapter used for transmitting audio signals to at least one headphone of the headset and receiving audio signals from the microphone assembly of the headset, the host adapter comprising:

a memory interface for accessing the memory device in order to read a first of set of performance characteristics of the microphone assembly stored in the digital memory device of the headset.

- 68. The host adapter of claim 67, further comprising filtering and filtering and compensation circuitry for adjusting the audio signals received from the microphone assembly of the headset as a function of the performance characteristics of the microphone assembly read from the memory.
- 69. The apparatus as claimed in claim 67, wherein the performance characteristics of the microphone assembly include at least one of a transmit signal frequency response, a transmit signal sensitivity, a transmit impedance characteristic, a transmit signal gain, and a transmit signal to noise ratio.



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70. A host adapter coupled to a headset having at least one headphone
a microphone assembly and a memory device, the host adapter used for
transmitting audio signals to at least one headphone of the headset and receiving
audio signals from the microphone assembly of the headset, the host adapter
comprising:
a mamory interface for accessing the memory device in order to

a memory interface for accessing the memory device in order to read a second of set of performance characteristics of the at least one headphone stored in the digital memory device of the headset.

- 71. The host adapter of claim 60, further comprising filtering and compensation circuitry for adjusting the audio signals transmitted to the at least one headphone of the headset as a function of the performance characteristics of the at least one headphone read from the memory.
- 72. The apparatus as claimed in claim 67, wherein the performance characteristics of the at least one headphone include at least one of a receive signal frequency response, a receive signal sensitivity, a receive impedance characteristic, a receive signal gain, and a receive signal to noise ratio.